

CLAIM AMENDMENTS

Please amend the claims as follows:

1. (Currently Amended) Apparatus ~~for use in boot up of an electronic device which includes a motherboard and a daughterboard comprising:~~ first data storage device, accessible to ~~said a~~ motherboard, storing daughterboard boot-up code; a coupler, coupling ~~said a~~ daughterboard to said motherboard, defining at least a first data communication path from said motherboard to said daughterboard; a microprocessor positioned on said daughterboard, wherein said microprocessor includes a development port, wherein the development port receives data from an emulator device external to the microprocessor when the development port is coupled to the emulator device; and at least a second communication path, defined on said daughterboard, providing for communication from said coupler to said development port; wherein ~~said a~~ boot-up code can be provided from said storage device, over said first communication path, said coupler and said second communication pathway, to said development port of said microprocessor on said daughterboard.

2. (Original) Apparatus, as claimed in Claim 1, wherein said motherboard is configured to download at least said boot-up code, to said development port automatically, in response to a power up or a reset of said electronic device.

3. (Currently Amended) Apparatus, as claimed in Claim 1, wherein said daughterboard includes a DRAM and a memory controller and wherein said boot-up code includes comprises configuration information for configuring the memory controller configuration information.

4. (Currently Amended) A method for performing boot-up in an electronic device ~~including comprising~~ a motherboard and a coupled daughterboard, said daughterboard ~~including comprising~~ a microprocessor ~~having, said microprocessor comprising~~ a development port, ~~the method comprising:~~

automatically downloading at least first boot-up code from said motherboard to said development port, in response to a power-on or reset of said electronic device, ~~wherein the development port receives data from an emulator device external to the microprocessor when the development port is coupled to the emulator device;~~ and

using said boot-up code, in said microprocessor of said daughterboard, for performing at least a first boot-up operation.

5. (Currently Amended) A method, as claimed in Claim 4, wherein said boot-up operation ~~includes comprises~~ configuring a port, different from said development port.

6. (Currently Amended) A method, as claimed in Claim 4, wherein said daughterboard ~~includes comprises~~ a DRAM and a memory controller, and wherein said boot-up operation comprises configuring said memory controller.

7. (Original) A method, as claimed in Claim 4, further comprising downloading at least a portion of an operating system for said microprocessor, from said motherboard, using said development port.

8. (Original) A method, as claimed in Claim 4, wherein said step of downloading said at least first boot-up code is performed while said daughterboard is coupled to said motherboard.

9. (Cancelled)

10. (Original) A method, as claimed in Claim 4, wherein said first boot-up operation is performed in the absence of storing said boot-up code on a daughterboard non-volatile memory prior to said power-up or reset.

11. (Currently Amended) Apparatus for performing boot up in an electronic device ~~including comprising~~ a motherboard and a coupled daughterboard, said daughterboard ~~including comprising~~ a microprocessor, ~~the microprocessor comprising having~~ a development port, comprising:

means for automatically downloading at least first boot up code from said motherboard to said development port, in response to a power on or reset of said electronic device, ~~wherein the development port receives data from an emulator device external to the microprocessor when the development port is coupled to the emulator device;~~ and
means for performing at least a first boot-up operation, using said boot-up code, in said microprocessor of said daughterboard.

12. (Currently Amended) Apparatus, as claimed in Claim 11, wherein said means for performing said first boot-up operation ~~includes comprising~~ means for configuring a port, different from said development port.

13. (Currently Amended) Apparatus, as claimed in claim 11, wherein said means for performing said first boot-up operation ~~includes comprising~~ means for initializing DRAM chip selects.

14. (Currently Amended) Apparatus, as claimed in Claim 11, wherein said daughterboard ~~includes comprising~~ a DRAM and a memory controller, and wherein said means for performing said first boot up operation comprises means for configuring said memory controller.

15. (Original) Apparatus, as claimed in Claim 11, further comprising means for downloading at least a portion of an operating system for said microprocessor, from said motherboard, using said development port.

16. (Original) Apparatus, as claimed in Claim 11, wherein said means for automatically downloading includes means for downloading while said daughterboard is coupled to said motherboard.

17. (Cancelled).

18. (Currently Amended) Apparatus, as claimed in Claim 11, wherein said means for performing said first boot-up operation ~~includes comprising~~ means for performing said first boot-up operation in the absence of storing said boot-up code on a daughterboard non-volatile memory prior to said power-up or reset.